

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	229	(712/15).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L2	135	(712/20).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L3	80	(712/21).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L4	317	(712/22).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L5	141	SIMD near4 MIMD near4 (hybrid\$1 or combin\$5 or parallel\$3 or simulatneous\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:59
L6	56	SIMD near4 MIMD near4 (hybrid\$1 or combin\$5 or parallel\$3 or simulatneous\$3) near4 (instruction\$1 or operat\$5 or execut\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:59
L7	15	SIMD near4 MIMD near4 (hybrid\$1 or combin\$5 or simulatneous\$3) near4 (instruction\$1 or operat\$5 or execut\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:59
L8	75	(SIMD near4 MIMD) with (hybrid\$1 or combina\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:11
L9	43	(SIMD near4 MIMD) with (hybrid\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:18
L10	6	("5734921") or ("5754871") or ("5761523") or ("5828894") or ("5966528") or ("6094715").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:20
L11	124	manarray	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:20

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L12	0	manarray with SIMD with MIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:20
L13	0	manarray same (SIMD with MIMD)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:22
L14	7	manarray and (SIMD with MIMD)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:22
L15	3	((chen-inching\$) and (honary-hooman\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:44
L16	41	((chen-inching\$) or (honary-hooman\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:44
S1	209	(712/15).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:57
S2	135	(712/20).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:57
S3	78	(712/21).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:57
S4	285	(712/22).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:59
S5	96	SIMD with MIMD with switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S6	31	SIMD with MIMD with switch\$3 with control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:01
S7	0	SIMIMD with switch\$3 with control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:01

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S8	21	SIMIMD same (switch\$3 with control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:09
S9	31	SIMIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:09
S10	56	SIMD with MIMD with configur\$5	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S11	48	SIMD near4 MIMD near4 configur\$5	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S12	73	SIMD near4 MIMD near4 switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S13	0	SIMD near4 MIMD near4 configur\$5 near4 switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:18
S14	0	SIMD near4 MIMD near4 switch\$3 near4 mesh\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:12
S15	0	((SIMD near4 MIMD) or SIMIMD) with (switch\$3 near4 mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:12
S16	0	((SIMD near4 MIMD) or SIMIMD) same (switch\$3 near4 mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S17	0	((SIMD with MIMD) or SIMIMD) same (switch\$3 near4 mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S18	0	((SIMD with MIMD) or SIMIMD) same (switch\$3 with mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S19	23	((SIMD with MIMD) or SIMIMD) same (mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13

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S20	1	((SIMD with MIMD) or SIMIMD) with (mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S21	4	SIMD near4 control\$4 near4 switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:19
S22	79	SIMD with control\$4 with switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:07
S23	813	SIMD with control\$4 with process\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:19
S24	0	(gove-r).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:08
S25	226	(gove).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:08
S26	16	(gove).in. and (712/??).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:13
S27	1	("20050216700").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:13



SIMD + MIMD + (hybrid or combination) + F

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... execution In SIMD mode, performance of conditional statements, and the consequences of switching parallelism modes. 1. Introduction Most large-scale parallel ...

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... Hybrid computing denotes the combination of the SIMD and ... We have built a hybrid MIMD-SIMD architecture from general available ... v. 1.1.2. For the SIMD part we ...

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... wards providing a fine grain MIMD execution model for ... controlled, and operate in parallel on different ... computation is similar to SIMD execution, except that ...

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Data management and control-flow aspects of an SIMD/SPMD parallellanguage/compiler - group of 5 »

MA Nichols, HJ Siegel, HG Dietz - Parallel and Distributed Systems, IEEE Transactions on, 1993 - [ieeexplore.ieee.org](#)
... and OPSILA [2], [3], [16] is a hybrid SIMD/SPMD machine. ... a combination of a C language compiler and AWK scripts ... SIMD mode is the default mode of execution, and ...

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... CMS, so that it primarily supports a SIMD data parallel ... The CMS is a hybrid machine with each computation ... parallel domains, tiling or any combination ofthese. ...

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... instruction multiple data (SIMD) and multiple instruction multiple data (MIMD) modes and method of ... - group of 2 »

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... "A Single Board Image Computer with 64 Parallel Processors", by ... Each processor can operate to execute the same instruction at the same time (SIMD mode) or ...

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... The syntax of ELP is based on C, extended to sup- port parallel execution. ELF currently includes constructs for SIMD and SPMD (single program - multiple data ...

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... The syntax of ELP is based on C; extended to support parallel execution. ELF currently includes constructs for SIMD and SPMD (single program - multiple data ...
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NB Serbedzija - Computer, 1996 - ieeexplore.ieee.org

... a single input), and • weight parallelism (**simultaneous** weighted summa ... Coarse Fine Digital Analog Hybrid Optical (CM ... arrays PC boards Systolic SIMD SPMD Figure ...

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» NK Ing-Simmons, KM Guttag, RJ Gove, K Balmer - US Patent 5,239,654, 1993 - Google Patents

Page 1. United States Patent Ing-Simmons et al. [54] DUAL MODE SIMD/MIMD PROCESSOR ...

FIG. 9 (PRIOR ART) RECONFIGURABLE SIMD/MIMD LOCAL AND GLOBAL INTERCONNECTION ...

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[Volume Visualisation on a Hybrid Parallel Architecture](#)

B Schmidt, H Schröder - ntu.edu.sg

... **Hybrid** computing is the **combination** of the SIMD and MIMD paradigm ... In this paper we have presented a **hybrid** architecture, combining the SIMD and MIMD ...

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... Gove et al. [54] MULTI-PROCESSOR RECONFIGURABLE IN SINGLE INSTRUCTION MULTIPLE DATA (SIMD) AND MULTIPLE INSTRUCTION ... INTERCONNECT NETWORK SIMD 81 FIG. 7 FIG. 5 ...

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T Hughes, C Hughes - 2003 - books.google.com

... S. 1 Coordinating Order of Execution 185 S. 1. 1 ... 6.2.5.2 Using the MPMD (MIMD) Model with PVM and ... The C++ Standard Library in **combination** with CORBA and the ...

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VS Sunderam, G Geist - Parallel Computing, 1999 - ace.ual.es

... Coarse-grained MIMD machines are best matched for ... message passing, shared-memory, and **hybrid** paradigms; thus ... The **combination** of these features allows parallel ...

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[A Grimm Collection of MIMD Fairy Tales](#)

T Blank, JK Nicolls - ieeexplore.ieee.org

... 4], and one can build **hybrid** machines [5 ... further distinguishes it from SIMD vector architectures. ... Execution Autonomy Addressing Autonomy Connection Autonomy I/O ...

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[book] [Reporting Computational Experiments with Parallel Algorithms: Issues, Measures, and Experts' ...](#)

RS Barr, BL Hickman - 1992 - faculty.smu.edu

... simultaneous pricing and pivoting, parallel pricing of ... of the performance of an MIMD parallel implementation ... dependent on a particular **combination** of algorithm ...

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[Parallelism in Knowledge Discovery Techniques - group of 4 »](#)

D Talia - Proceedings of the 6th International Conference on Applied ..., 2002 - Springer

... can be combined to form different **hybrid** parallelization ... Different **combinations**

can raise different issues to be ... results on distributed memory MIMD machines. ...

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